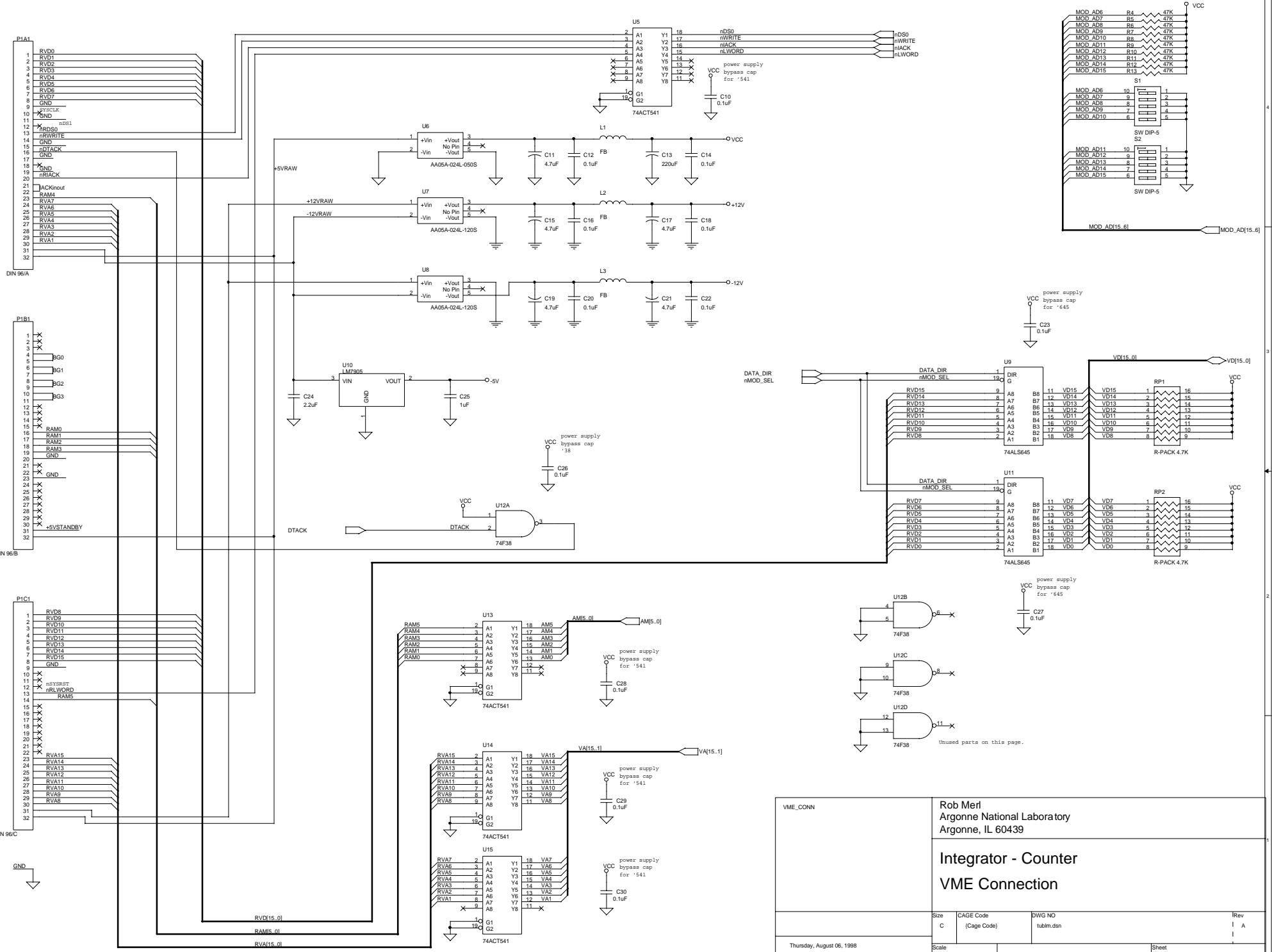
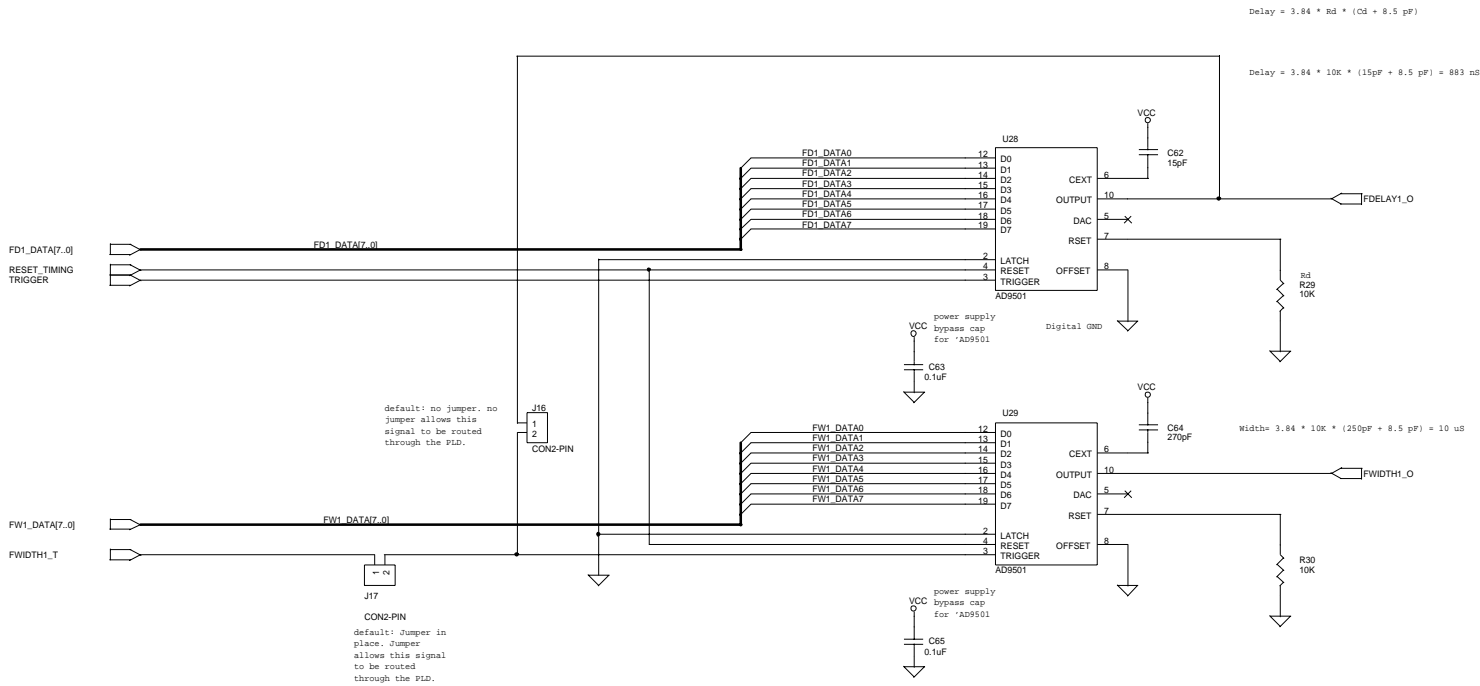


References and properties updated from LOGICAL view.

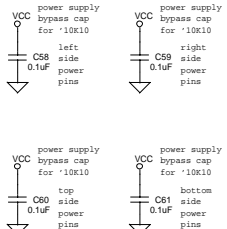
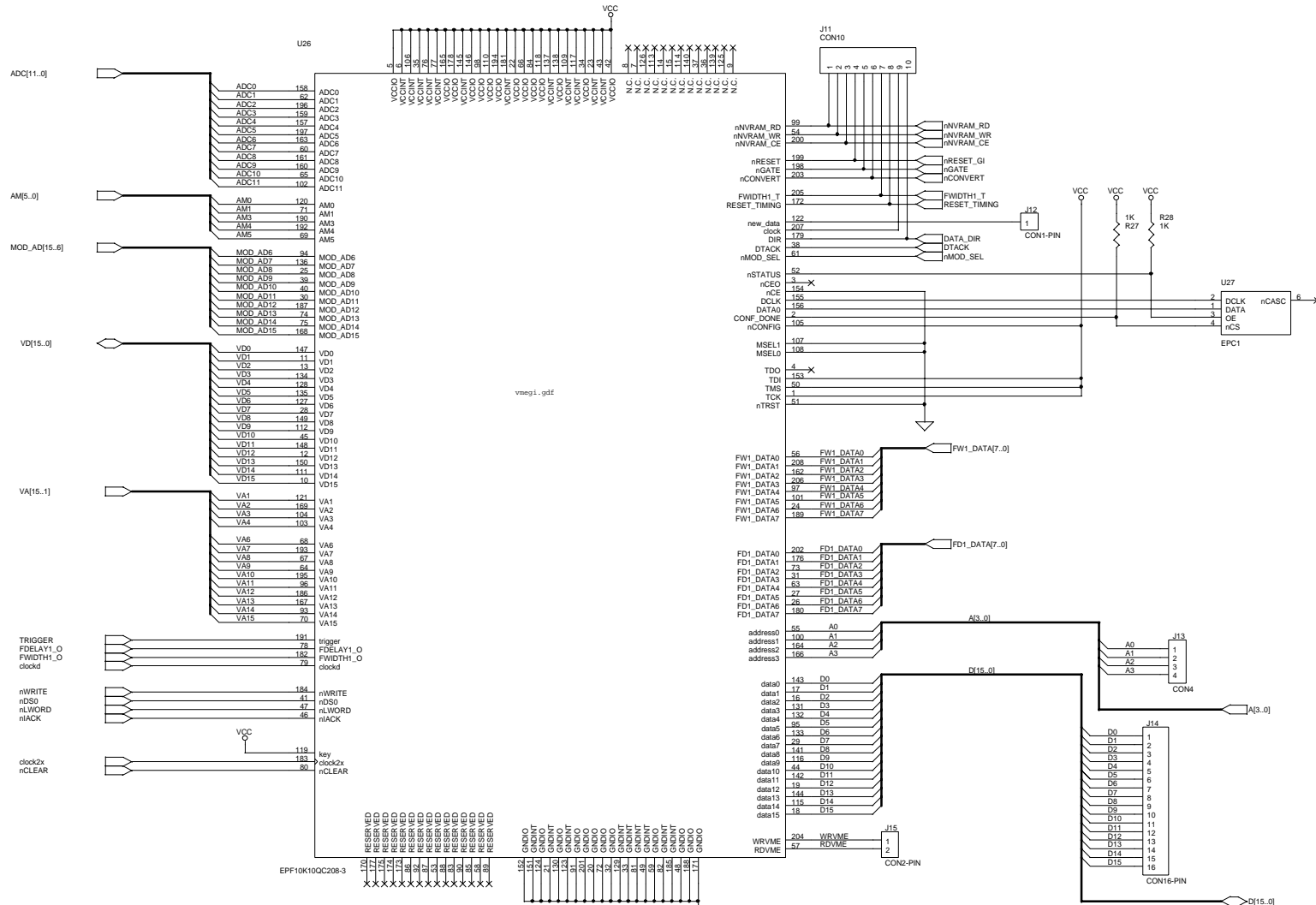
TOP	Rob Merl Argonne National Laboratory Argonne, IL 60439		
	Gated Integrator VME Card Top Level		
Size C	CAGE Code (Cage Code)	DWG NO vmegi.dsn	Rev I A I
Thursday, August 06, 1998	Scale	Sheet 1	of 8



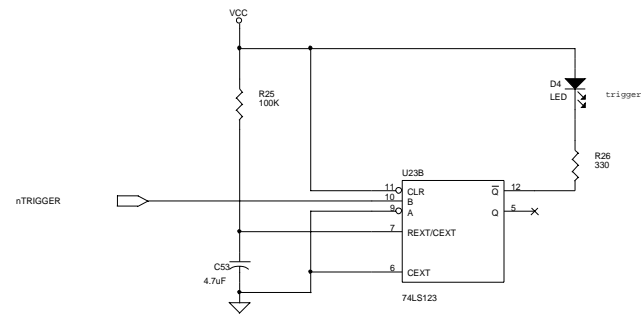
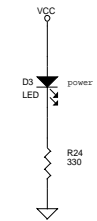
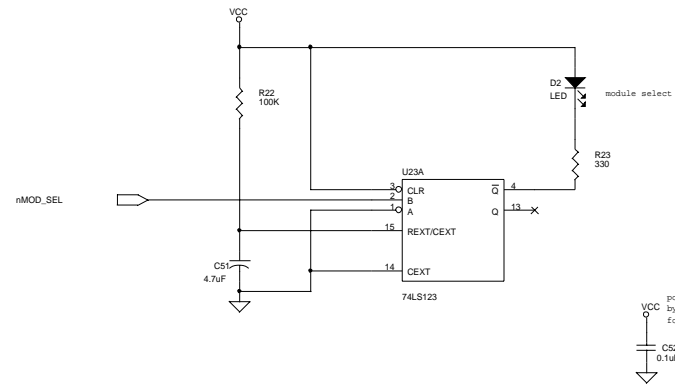
VME_CONN		Rob Merl Argonne National Laboratory Argonne, IL 60439	
Integrator - Counter			
VME Connection			
Size	CAGE Code	DWG NO	lRev
C	(Cage Code)	tublm.dsn	I A
Thursday, August 06, 1998		Scale	Sheet 2 of 8



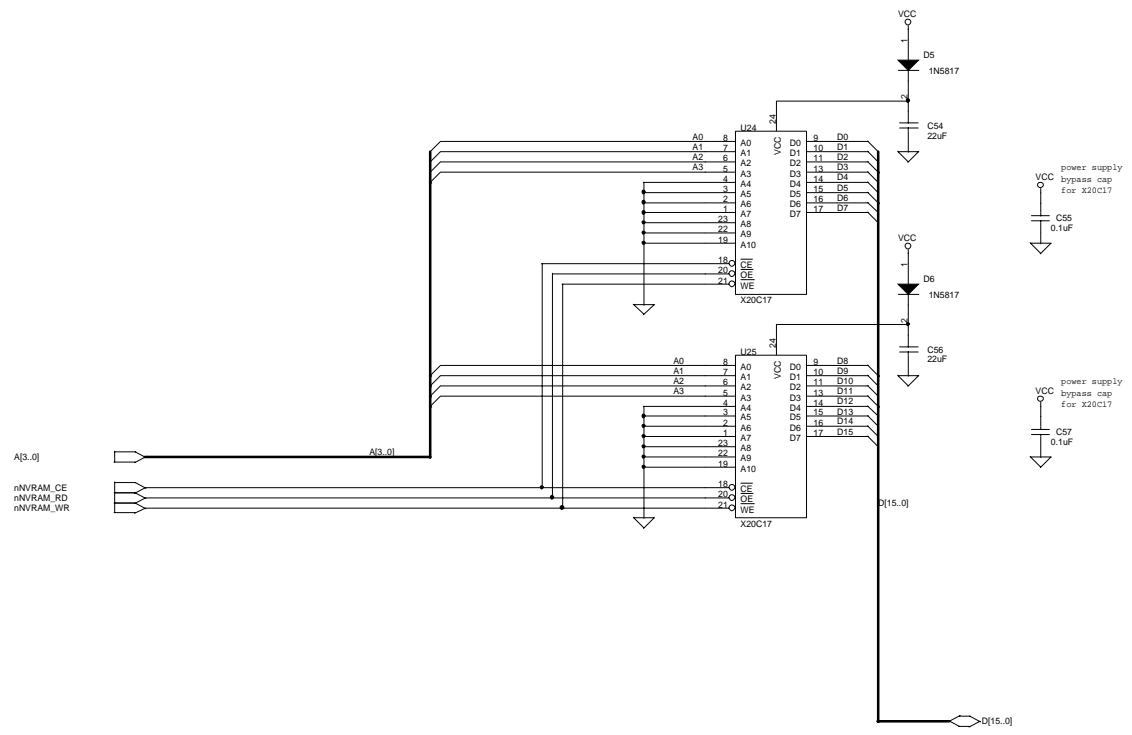
DELAY	Rob Merl Argonne National Laboratory Argonne, IL 60439		
	Gated Integrator VME Board Gate Delay and Width Generator		
Size	CAGE Code (Cage Code)	DWG NO vmeqi.dsn	Rev I A I
Thursday, August 06, 1998		Scale	Sheet 3 of 8



VMEGI_PLD		Rob Merl Argonne National Laboratory Argonne, IL 60439	
Gated Integrator VME Board PLD			
Size	CAGE Code (Cage Code)	DWG NO tublm.dsn	Rev I A
Thursday, August 06, 1998	Scale	Sheet	4 of 8

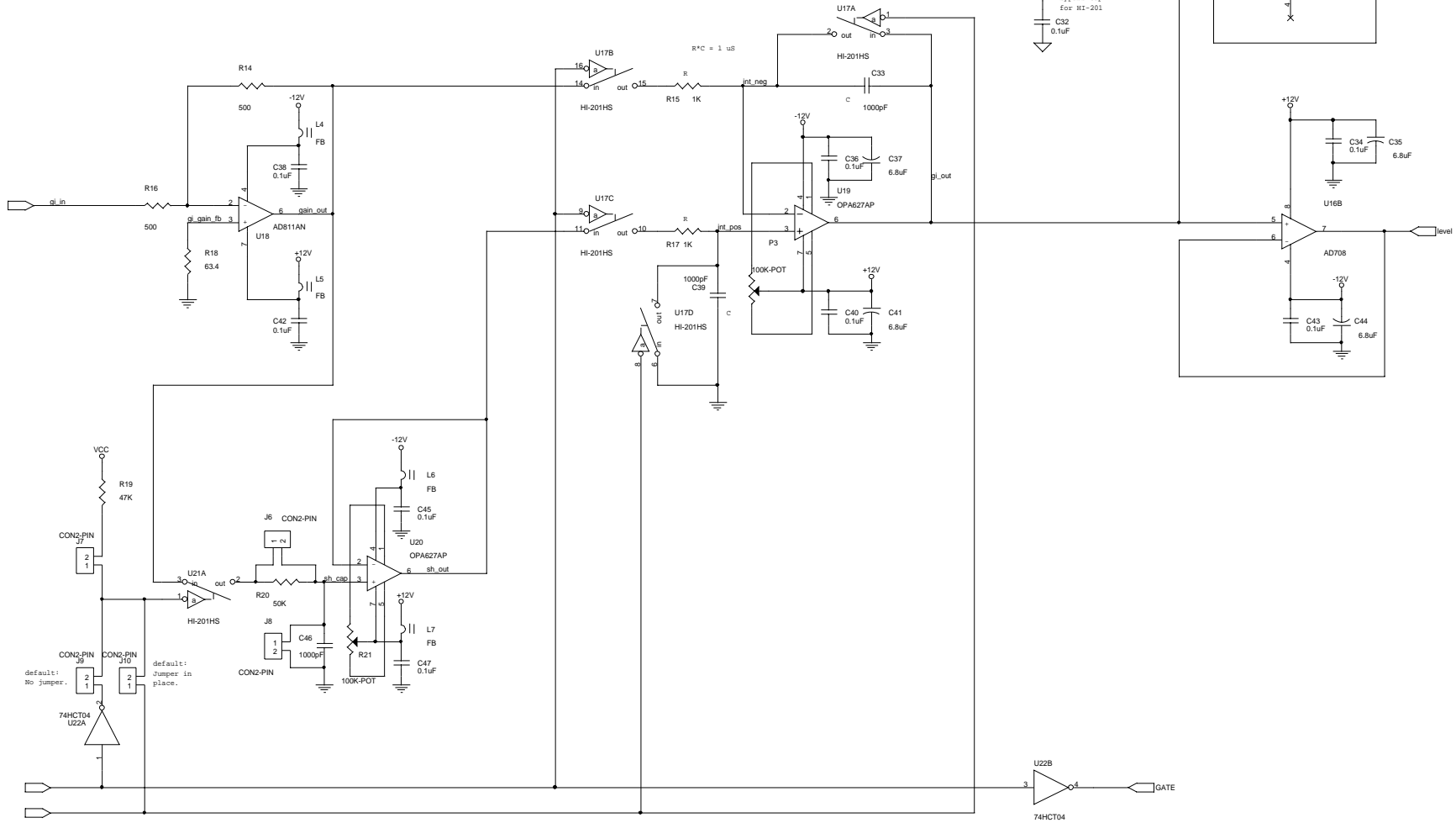


LEDS	Rob Merl Argonne National Laboratory Argonne, IL 60439		
	Integrator - Counter LEDS		
Size C	CAGE Code (Cage Code)	DWG NO tublm.dsn	Rev I A I
Thursday, August 06, 1998		Scale	Sheet 5 of 8

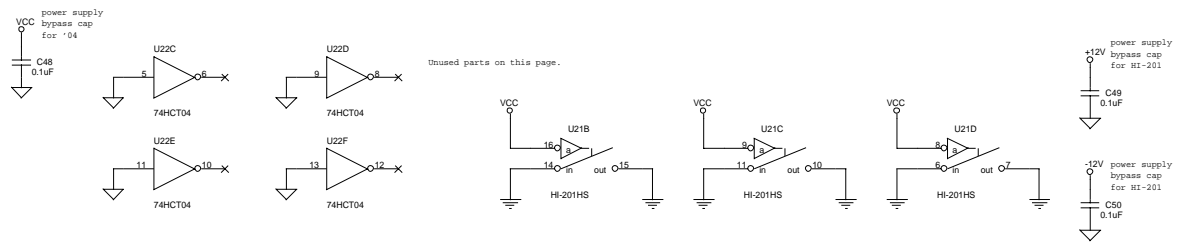


NOVRAM	Rob Merl Argonne National Laboratory Argonne, IL 60439		
	Gated Integrator VME Board Non Volatile Memory		
Size C	CAGE Code (Cage Code)	DWG NO vmegi.dsn	Rev I A I
Wednesday, August 05, 1998	Scale	Sheet	6 of 8

$V_o = 1/(RC) * \text{integral}(V_i(t))$, from 0 to t
 $1/RC = 1e-6$
 with a 5 volt DC input, the output = 15V in 3 us.
 the integrator saturates at 12V

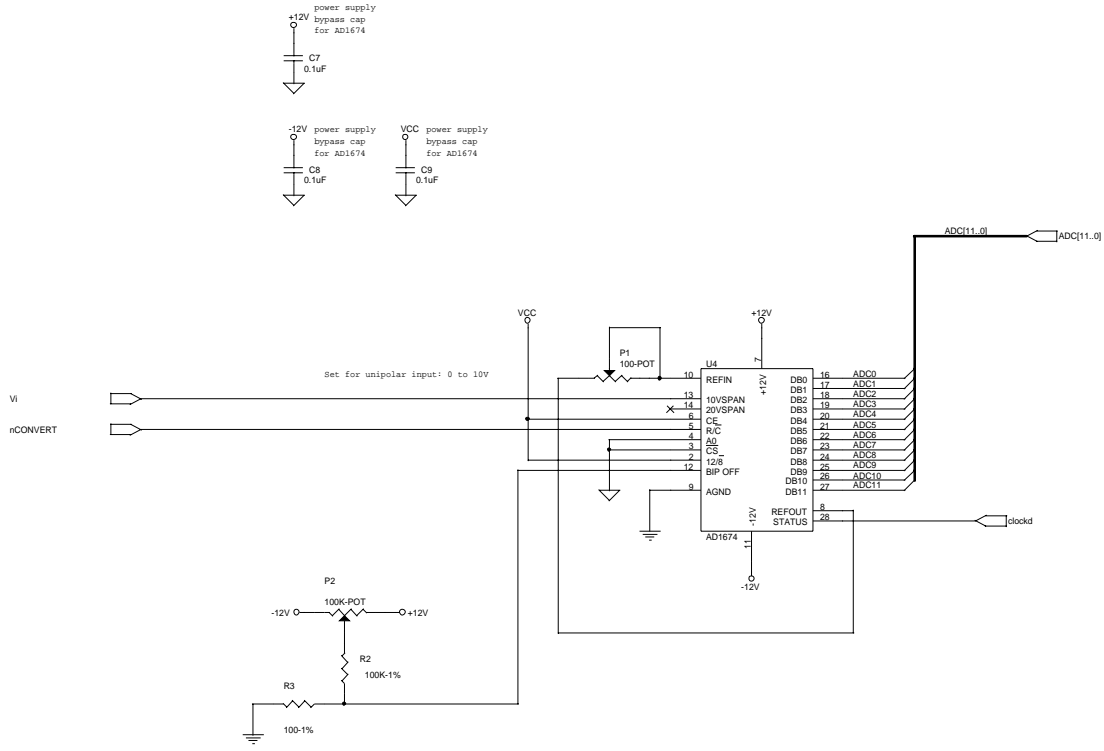


Unused parts on this page.



GI
 Circuit on this page is based on X. Wang's Gated Integrator.
 Thursday, August 06, 1998

Rob Merl Argonne National Laboratory Argonne, IL 60439	
Gated Integrator VME Board Gated Integrator	
Size C	CAGE Code (Cage Code)
DWG NO vmegi.dsn	Rev I A I
Scale	Sheet 7 of 8



ADC16	Rob Merl Argonne National Laboratory Argonne, IL 60439		
	Gated Integrator VME Board 12 Bit Analog to Digital Converter		
Size C	CAGE Code (Cage Code)	DWG NO vmeqi.dsn	Rev I A I
Thursday, August 06, 1998	Scale	Sheet 8	of 8