



Bunch Clock Generator Module

Function: Developed for beam line users to trigger data acquisition systems.

Type: VME

Resolution-Accuracy

Clock Speed

Memory Size

Power Requirements: 5v

The BCG100 is a VME module that generates a Storage Ring (SR) bunch clock or timing pulse for each stored bunch. An onboard RAM holds the bunch pattern. Each bit in the RAM corresponds to a SR bucket. The pattern in the RAM is shifted out serially at the SR rf rate to produce the bunch clock. This is referenced to P0 SR revolution clock.

For a complete explanation of the BCG100 [visit here](#).

Features:

- Provides bunch pattern and timing information
- Generates a timing pulse for each stored bunch
- Delays : Course -0-1295 tics
Fine-0 to 4.9ns n 18ps steps
- Referenced to SR rf and SR revolution clock
- Nim outputs – 1.5ns and raw
- EPICS automatically loads SR fill pattern at injection time
- 352 MHz distribution system

BCG100



[BCG100 web page](#)

